

The listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1-34. (Canceled)

35. (Currently Amended) A display device comprising:

a pair of [[filmy]] resinous substrates facing each other, wherein at least one of the pair of resinous substrates comprises polyethylene naphthalate;

a thin film transistor formed over one of the pair of [[filmy]] resinous substrates;

a layer comprising a resinous material covering the thin film transistor; and

a pixel electrode formed over the layer, and electrically connected to the thin film transistor.

36. (Currently Amended) A display device comprising:

a pair of flexible substrates facing each other, wherein at least one of the pair of flexible substrates comprises polyethylene naphthalate;

a thin film transistor formed over one of the pair of flexible substrates;

a layer comprising a resinous material covering the thin film transistor; and

a pixel electrode formed over the layer, and electrically connected to the thin film transistor.

37. (Currently Amended) A display device comprising:

a pair of [[filmy]] resinous substrates facing each other, wherein at least one of the pair of resinous substrates comprises polyethylene naphthalate;

a thin film transistor formed over one of the pair of [[filmy]] resinous substrates;

a silicon oxide film covering the thin film transistor, wherein the silicon oxide film is formed by applying a liquid; and

a pixel electrode formed over the silicon oxide film, and electrically connected to the thin film transistor.

38. (Currently Amended) A display device comprising:

a pair of flexible substrates facing each other, wherein at least one of the pair of flexible substrates comprises polyethylene naphthalate;

a thin film transistor formed over one of the pair of flexible substrates;

a silicon oxide film covering the thin film transistor, wherein the silicon oxide film is formed by applying a liquid; and

a pixel electrode formed over the silicon oxide film, and electrically connected to the thin film transistor.

39. (Currently Amended) A display device comprising:

a pair of [[filmy]] resinous substrates facing each other, wherein at least one of the pair of resinous substrates comprises polyethylene naphthalate;

a thin film transistor formed over one of the pair of [[filmy]] resinous substrates;

a layer comprising a resinous material covering the thin film transistor; and

a pixel electrode formed over the layer, and electrically connected to the thin film transistor,

wherein a resinous layer is provided on a surface of one of the pair of [[filmy]] resinous substrates.

40. (Currently Amended) A display device comprising:

a pair of flexible substrates facing each other, wherein at least one of the pair of flexible substrates comprises polyethylene naphthalate;

a thin film transistor formed over one of the pair of flexible substrates;

a layer comprising a resinous material covering the thin film transistor; and
a pixel electrode formed over the layer, and electrically connected to the thin film transistor,
wherein a resinous layer is provided on a surface of one of the pair of flexible substrates.

41. (Currently Amended) A display device comprising:
a pair of [[filmy]] resinous substrates facing each other, wherein at least one of the pair of resinous substrates comprises polyethylene naphthalate;
a thin film transistor formed over one of the pair of [[filmy]] resinous substrates;
a silicon oxide film covering the thin film transistor, wherein the silicon oxide film is formed by applying a liquid; and
a pixel electrode formed over the silicon oxide film, and electrically connected to the thin film transistor,
wherein a resinous layer is provided on a surface of one of the pair of [[filmy]] resinous substrates.

42. (Currently Amended) A display device comprising:
a pair of flexible substrates facing each other, wherein at least one of the pair of flexible substrates comprises polyethylene naphthalate;
a thin film transistor formed over one of the pair of flexible substrates;
a silicon oxide film covering the thin film transistor, wherein the silicon oxide film is formed by applying a liquid; and
a pixel electrode formed over the silicon oxide film, and electrically connected to the thin film transistor,
wherein a resinous layer is provided on a surface of one of the pair of flexible substrates.

43. (Currently Amended) A display device comprising:

a pair of [[filmy]] resinous substrates facing each other, wherein at least one of the pair of resinous substrates comprises polyethylene naphthalate;

a thin film transistor formed over one of the pair of [[filmy]] resinous substrates, wherein the thin film transistor has a channel formation region comprising amorphous silicon;

a layer comprising a resinous material covering the thin film transistor; and

a pixel electrode formed over the layer, and electrically connected to the thin film transistor,

wherein a resinous layer is provided on a surface of one of the pair of [[filmy]] resinous substrates.

44. (Currently Amended) A display device comprising:

a pair of flexible substrates facing each other, wherein at least one of the pair of flexible substrates comprises polyethylene naphthalate;

a thin film transistor formed over one of the pair of flexible substrates, wherein the thin film transistor has a channel formation region comprising amorphous silicon;

a layer comprising a resinous material covering the thin film transistor; and

a pixel electrode formed over the layer, and electrically connected to the thin film transistor,

wherein a resinous layer is provided on a surface of one of the pair of flexible substrates.

45. (Currently Amended) A display device comprising:

a pair of [[filmy]] resinous substrates facing each other, wherein at least one of the pair of resinous substrates comprises polyethylene naphthalate;

a thin film transistor formed over one of the pair of [[filmy]] resinous substrates, wherein the thin film transistor has a channel formation region comprising amorphous silicon;

a silicon oxide film covering the thin film transistor, wherein the silicon oxide film is formed by applying a liquid; and

a pixel electrode formed over the silicon oxide film, and electrically connected to the thin film transistor,

wherein a resinous layer is provided on a surface of one of the pair of [[filmy]] resinous substrates.

46. (Currently Amended) A display device comprising:

a pair of flexible substrates facing each other, wherein at least one of the pair of flexible substrates comprises polyethylene naphthalate;

a thin film transistor formed over one of the pair of flexible substrates, wherein the thin film transistor has a channel formation region comprising amorphous silicon;

a silicon oxide film covering the thin film transistor, wherein the silicon oxide film is formed by applying a liquid; and

a pixel electrode formed over the silicon oxide film, and electrically connected to the thin film transistor,

wherein a resinous layer is provided on a surface of one of the pair of flexible substrates.

47. (Currently Amended) A display device comprising:

a pair of [[filmy]] resinous substrates facing each other, wherein at least one of the pair of resinous substrates comprises polyethylene naphthalate;

a thin film transistor formed over one of the pair of [[filmy]] resinous substrates, wherein the thin film transistor has a channel formation region comprising microcrystalline silicon;

a layer comprising a resinous material covering the thin film transistor; and
a pixel electrode formed over the layer, and electrically connected to the thin film transistor,

wherein a resinous layer is provided on a surface of one of the pair of [[filmy]] resinous substrates.

48. (Currently Amended) A display device comprising:

a pair of flexible substrates facing each other, wherein at least one of the pair of flexible substrates comprises polyethylene naphthalate;

a thin film transistor formed over one of the pair of flexible substrates, wherein the thin film transistor has a channel formation region comprising microcrystalline silicon;

a layer comprising a resinous material covering the thin film transistor; and
a pixel electrode formed over the layer, and electrically connected to the thin film transistor,

wherein a resinous layer is provided on a surface of one of the pair of flexible substrates.

49. (Currently Amended) A display device comprising:

a pair of [[filmy]] resinous substrates facing each other, wherein at least one of the pair of resinous substrates comprises polyethylene naphthalate;

a thin film transistor formed over one of the pair of [[filmy]] resinous substrates, wherein the thin film transistor has a channel formation region comprising microcrystalline silicon;

a silicon oxide film covering the thin film transistor, wherein the silicon oxide film is formed by applying a liquid; and

a pixel electrode formed over the silicon oxide film, and electrically connected to the thin film transistor,

wherein a resinous layer is provided on a surface of one of the pair of [[filmy]] resinous substrates.

50. (Currently Amended) A display device comprising:

a pair of flexible substrates facing each other, wherein at least one of the pair of flexible substrates comprises polyethylene naphthalate;

a thin film transistor formed over one of the pair of flexible substrates, wherein the thin film transistor has a channel formation region comprising microcrystalline silicon;

a silicon oxide film covering the thin film transistor formed, wherein the silicon oxide film is formed by applying a liquid; and

a pixel electrode formed over the silicon oxide film, and electrically connected to the thin film transistor,

wherein a resinous layer is provided on a surface of one of the pair of flexible substrates.

51. (Currently Amended) A display device comprising:

a pair of [[filmy]] resinous substrates facing each other, wherein at least one of the pair of resinous substrates comprises polyethylene naphthalate;

a thin film transistor formed over one of the pair of [[filmy]] resinous substrates, wherein the thin film transistor has a channel formation region comprising crystalline silicon;

a layer comprising a resinous material covering the thin film transistor; and

a pixel electrode formed over the layer, and electrically connected to the thin film transistor,

wherein a resinous layer is provided on a surface of one of the pair of [[filmy]] resinous substrates.

52. (Currently Amended) A display device comprising:

a pair of flexible substrates facing each other, wherein at least one of the pair of flexible substrates comprises polyethylene naphthalate;

a thin film transistor formed over one of the pair of flexible substrates, wherein the thin film transistor has a channel formation region comprising crystalline silicon;

a layer comprising a resinous material covering the thin film transistor; and

a pixel electrode formed over the layer, and electrically connected to the thin film transistor,

wherein a resinous layer is provided on a surface of one of the pair of flexible substrates.

53. (Currently Amended) A display device comprising:

a pair of [[filmy]] resinous substrates facing each other, wherein at least one of the pair of resinous substrates comprises polyethylene naphthalate;

a thin film transistor formed over one of the pair of [[filmy]] resinous substrates, wherein the thin film transistor has a channel formation region comprising crystalline silicon;

a silicon oxide film covering the thin film transistor, wherein the silicon oxide film is formed by applying a liquid; and

a pixel electrode formed over the silicon oxide film, and electrically connected to the thin film transistor,

wherein a resinous layer is provided on a surface of one of the pair of [[filmy]] resinous substrates.

54. (Currently Amended) A display device comprising:

a pair of flexible substrates facing each other, wherein at least one of the pair of flexible substrates comprises polyethylene naphthalate;

a thin film transistor formed over one of the pair of flexible substrates, wherein the thin film transistor has a channel formation region comprising crystalline silicon;

a silicon oxide film covering the thin film transistor, wherein the silicon oxide film is formed by applying a liquid; and

a pixel electrode formed over the silicon oxide film, and electrically connected to the thin film transistor,

wherein a resinous layer is provided on a surface of one of the pair of flexible substrates.

55. (Currently Amended) A display device comprising:

a pair of [[filmy]] resinous substrates facing each other, wherein at least one of the pair of resinous substrates comprises polyethylene naphthalate;

a thin film transistor formed over one of the pair of [[filmy]] resinous substrates, wherein the thin film transistor has a channel formation region comprising crystalline silicon formed by irradiating an amorphous silicon film with a laser light;

a layer comprising a resinous material covering the thin film transistor; and

a pixel electrode formed over the layer, and electrically connected to the thin film transistor,

wherein a resinous layer is provided on a surface of one of the pair of [[filmy]] resinous substrates.

56. (Currently Amended) A display device comprising:

a pair of flexible substrates facing each other, wherein at least one of the pair of flexible substrates comprises polyethylene naphthalate;

a thin film transistor formed over one of the pair of flexible substrates, wherein the thin film transistor has a channel formation region comprising crystalline silicon formed by irradiating an amorphous silicon film with a laser light;

a layer comprising a resinous material covering the thin film transistor; and

a pixel electrode formed over the layer, and electrically connected to the thin film transistor,

wherein a resinous layer is provided on a surface of one of the pair of flexible substrates.

57. (Currently Amended) A display device comprising:

a pair of [[filmy]] resinous substrates facing each other, wherein at least one of the pair of resinous substrates comprises polyethylene naphthalate;

a thin film transistor formed over one of the pair of [[filmy]] resinous substrates, wherein the thin film transistor has a channel formation region comprising crystalline silicon formed by irradiating an amorphous silicon film with a laser light;

a silicon oxide film covering the thin film transistor, wherein the silicon oxide film is formed by applying a liquid; and

a pixel electrode formed over the silicon oxide film, and electrically connected to the thin film transistor,

wherein a resinous layer is provided on a surface of one of the pair of [[filmy]] resinous substrates.

58. (Currently Amended) A display device comprising:

a pair of flexible substrates facing each other, wherein at least one of the pair of flexible substrates comprises polyethylene naphthalate;

a thin film transistor formed over one of the pair of flexible substrates, wherein the thin film transistor has a channel formation region comprising crystalline silicon formed by irradiating an amorphous silicon film with a laser light;

a silicon oxide film covering the thin film transistor, wherein the silicon oxide film is formed by applying a liquid; and

a pixel electrode formed over the silicon oxide film, and electrically connected to the thin film transistor,

wherein a resinous layer is provided on a surface of one of the pair of flexible substrates.

59. (Previously Presented) A display device according to any one of claims 55-58, wherein the laser light comprises at least one selected from the group consisting of KrF excimer laser light and XeCl laser light.

60. (Previously Presented) A display device according to any one of claims 39-58, wherein the resinous layer comprises an acrylic resin.

61. (Previously Presented) A display device according to any one of claims 39-58, wherein the resinous layer comprises at least one selected from the group consisting of methyl esters of acrylic acid, ethyl esters of acrylic acid, butyl esters of acrylic acid, and 2-ethylhexyl esters of acrylic acid.

62.-65. (Canceled)

66. (Previously Presented) A display device according to any one of claims 35-58, wherein the thin film transistor comprises a coplanar thin-film transistor.

67. (Previously Presented) A display device according to any one of claims 35-58, wherein the thin film transistor comprises an inverted-staggered thin-film transistor.